VSperf Home

Project: NFVI Network Performance for Telco NFV

VSperf provides an automated test-framework and comprehensive test suite based on industry standards for measuring data-plane performance which includes switching technologies with physical and virtual network interfaces. The VSperf architecture is switch and traffic generator agnostic and test cases can be customized. The vSwitch and other software component versions and configurations are controlled by VSperf as is the network topology. VSperf is useful for development of switching technologies as well as for evaluation and optimization of the data-path.

Details about vsperf installation, configuration, features and supported testcases are available at official documentation.

Key Project Facts

INFO (source from vswitchperf)

| Project: Data-plane performance testing and benchmarking |
| Creation Date: December 16th, 2014 |
| Category: Integration & Testing |
| Lifecycle State: Mature |
| Primary Contact: Sridhar Rao |
| Project Lead: (Sridhar Rao) sridhar.rao@spirent.com |
| Jira name vSwitch Characterization |
| JIRA tag : VSperf |
| Repository: vsmergeperf |
| Mailing list tag: [vsperf] |
| IRC: Server:freenode.net Channel: #opnfv-vswitchperf |

Committers:
mamyam.tahhan@intel.com
acmorton@att.com
ctrautma@redhat.com
martinx.klozik@intel.com
sridhar.rao@spirent.com
bmfichalo@redhat.com
trevor.cooper@intel.com

Link to TSC approval of the project: http://meetbot.opnfv.org/meetings/opnfv-meeting/2016-05-17-13.59.html

Contributors:

- Tapio Tallgren tapio.tallgren@nsn.com
- Julien Meunier julien.meunier@6wind.com
- Petteri Ylä-Outinen <petteri.yla-outinen@nsn.com>
- Mike Lynch Michael.a.lynn@intel.com
- John Browne john.j.browne@intel.com
- Trevor Cooper trevor.cooper@intel.com
- Raghu Kondapalli raghu.kondapalli@intel.com
- Sean Chen s.chen@huawei.com
- Mario Cho hephaex@gmail.com
- Mark Gray Mark.d.gray@intel.com
- Kevin Traynor Kevin.traynor@intel.com
- Andrea Pinnola andrea.pinnola@telecomitalia.it
- Bin Hu bh526r@att.com
- Prabhu Kuppuswamy prabhu.kuppuswamy@spirent.com
- Gurpreet Singh gurpreet.singh@spirent.com
- Glenn Seiler glenn.seiler@windriver.com
- Haruki Sonehara Haruki.Sonehara@aeroflex.com
- Tv Rao tv.rao@freescale.com
- Kin-Yip Liu kliu@cavium.com
- Billy O Mahony billy.o.mahony@intel.com
- Vishal Murgai Vishal.Murgai@caviumnetworks.com
- Dino SimeonX Madarang dino.simeonx.madarang@intel.com
- MartinX Klozik martinx.klozik@intel.com
- RadekX Zetik radekx.zetik@intel.com
- Brian Castell Brian.Castelli@spirent.com
- Velavarthipati Ramprasad-B42929 Ram.V@freescale.com
Processes and Methodology: VSPERF Test Methodology

Test Specification Etherpad: https://etherpad.opnfv.org/p/vSwitchPerfTestSpec

Coding Guidelines: OPNFV Coding Guidelines

OPNFV Developer Resource Page: Getting started with your account, Gerrit and Git

JIRA Getting Started Guide: Getting started with JIRA

Git, Gerrit, Documentation and Artifacts

Git: https://git.opnfv.org/vswitchperf

Gerrit: https://gerrit.opnfv.org/gerrit/#/q/vswitchperf . To Clone: https://gerrit.opnfv.org/gerrit/vswitchperf

Documentation: http://artifacts.opnfv.org/vswitchperf/docs/index.html

Branching Model: https://wiki.openstack.org/wiki/Branch_Model

Vswitchperf artifacts: http://artifacts.opnfv.org/vswitchperf.html

VSPERF TEST ENVIRONMENT (INTEL OPNFV LAB - POD 12)

- VSPERF Testing in Intel Pharos Lab (POD 12)
- Intel Pod 12 Network Configuration
- Intel Pod 12 Booking Calendar

Project Links

- Vswitch Project Proposal
- Traffic Generator Working Group
- Vswitch Release Plan
- Vswitchperf Contributions to Testing Specifications
- VSPERF CI

Test Results

- Traffic Generator Testing (Comparison of Traffic Generators)
- VSPERF CI Baseline Results

Documentation

- VSPERF Documentation for Danube

List of Candidate Work Items
• Vswitch Work Items
• Vswitch Test List

Project Meetings
• Meeting calendar of "vSwitch Performance"
• VSPERF Meeting Details + Minutes

Governance and Patch submission
• "vSwitch Performance Characterization" Governance Model proposal
• "vSwitch Performance Characterization" Patch Submission

Overviews
• Vswitch Readouts
• LTD Test Spec Overview
• Framework Overview
• VSPERF Deployment Topology Description Scheme

Test Results
• Vsperr Results

Golden Deck

VSPERF_Golden.pptx